Reg. No. Image: SidDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) M.Tech I Year I Semester Regular & Supplementary Examinations February 2018 Advanced Computer Architecture (Common to ES & DECS) Time: 3 hours Max. Marks:60 (Answer all Five Units 5 X 12 =60 Marks) UNIT:-1 a. Explain the concept of speed of hardware and its cost? 6M b. Explain the concept of speed of hardware and its cost? 6M b. Explain the old of compilers? 6M c. Explain in the old of compilers? 6M b. Explain instruction set architecture. 6M b. Discuss the CPU performance equation? 6M b. Differentiate hardware versus software approach of ILP? 6M b. Differentiate hardware versus software approach of ILP? 6M c. Suplain symmetric shared memory architecture. 6M b. Explain symmetric shared memory architecture. 6M c. Suplain symmetric shared performance equation? 6M b. Explain the hardware primitives for synchronization 6M c. Suplain in detail about bus design decisions. 6M c. Suplain in detail about bus design decisions. 6M d. Give details of cache performance equation? 6M c. Suplain in detail about bu	Q.P. C	0 B Codo:16EC5503											
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